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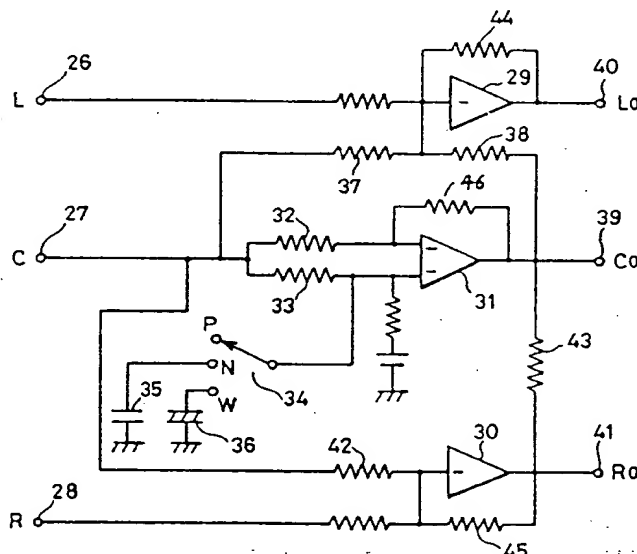
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(54) Center mode control circuit.

(57) A center mode control circuit comprises an amplifying portion (31) for amplifying a center input signal, a switch (34) connected to an input terminal of the amplifying portion, capacitors (35, 36) selected by the switch and having different characteristic from each other. The switched are a normal mode for adding low frequency component of the center input signal to left and right input signals, a phantom mode for adding full range of the center input signal to the left and the right stereo input signals, and a wide mode for not adding the center input signal to the left and the right stereo input signals.

FIG.1



SR

## Center Mode Control Circuit

### BACKGROUND OF THE INVENTION

#### Field of the Invention

The present invention relates to a center mode control circuit, and more specifically, to a center mode control circuit employed, for example, in a Dolby Pro Logic Surround Decoder, and suitable for an IC (Integrated Circuit).

### DESCRIPTION OF THE BACKGROUND ART

An article regarding to Dolby Pro Logic Surround System proposed by Dolby Laboratories Licensing Corp. of the United States appears on pp. 88-89 in "NIKKEI Electronics" issued on June 27, 1988. As shown in Fig. 2, the system is comprised of left and right input terminals 1 and 2 to which left and right stereo signals  $L_T$  and  $R_T$  applied, a first adder circuit 3 for adding the left and right stereo signals  $L_T$  and  $R_T$  to generate a sum signal  $C' (= L_T + R_T)$ , a subtractor circuit 4 for subtracting the right stereo signal  $R_T$  from the left stereo signal  $L_T$  to generate a difference signal  $S' (= L_T - R_T)$ , first and second detection circuits 5 and 6 for respectively detecting levels of the left and right stereo signals  $L_T$  and  $R_T$ , third and fourth detection circuits 7 and 8 for respectively detecting levels of the sum and difference signals  $C'$  and  $S'$ , a first level ratio detection circuit 9 for detecting level ratio of output signals of the first and second detection circuit 5 and 6, a second level ratio detection circuit 10 for detecting level ratio of output signals of the third and fourth detection circuits 7 and 8, a VCA (Voltage Controlled Amplifier) 11 including a plurality of gain controlled amplifier circuits (not shown) each controlling the level of the left or right stereo signal  $L_T$  or  $R_T$  in response to any one of output signals of the first and second level ratio detection circuits 9 and 10, a second adder circuit 12 for selectively adding the left and right stereo signals and output signals of the VCA 11 to generate left and right stereo output signals  $L$  and  $R$ , a center output signal  $C$  and a surround output signal  $S$ , a center mode control circuit 13 for switching between the left and right stereo output signals  $L$  and  $R$  and the center output signal  $C$  in response to a mode, and a passive decoder 14 for performing signal processing such as delay and noise reduction to the surround output signal  $S$ . Therefore, a structure such as shown in Fig. 2 enables a signal processing of enhancement of direction to clarify surround localization of sound,

so that acoustics having presence can be provided to listeners. Particularly, the system is effective when applied to an audio signal processing of a large-sized television, so that it can produce the same effect on audience as that is obtained in seeing a picture at a theater.

The center mode control circuit is disposed in order to switch for a normal mode, a phantom mode and a wide mode. In a normal mode, only low frequency component of the center output signal  $C$  is added to the left and the right stereo output signals  $L$  and  $R$ . In a phantom mode, full range of the center output signal  $C$  is added to the left and the right stereo output signals  $L$  and  $R$ . In a wide mode, nothing is added, and the left and the right stereo output signals  $L$  and  $R$  are generated as they are.

Fig. 3 is a circuit diagram showing an example of the conventional center mode control circuit, wherein left and right stereo input signals  $L$  and  $R$  applied to left and right input terminals 15 and 16 are directly applied to first and second adder circuit 17 and 18, respectively. A center input signal applied to a center input terminal 19 is passed through a low pass filter 20 or a high pass filter 21, or directly transferred to terminals of first and second switches 22 and 23. The signal passed through the first switch 22 is applied to an attenuation circuit 24, wherein it is attenuated by 3 dB to be applied to the first and the second adder circuits 17 and 18, while the signal passed through the second switch 23 is provided to a center output terminal 25 as a center output signal  $C_0$ .

Now, states of the left and the left stereo output signals  $L_0$  and  $R_0$  and the center output signal  $C_0$  in each mode will be described. In a normal mode, an output signal of the low pass filter 20 is applied to the first and the second adder circuits 17 and 18 through the first switch 22 and the attenuation circuit 24. As a result, the left stereo output signal  $L_0$  becomes a signal obtained by addition of the left stereo input signal  $L$  and high frequency cut off center input signal  $C_L$ , and the right stereo output signal  $R_0$  becomes a signal obtained by addition of the right stereo input signal  $R$  and a high frequency cut off center input signal  $C_L$ , and the center output signal  $C_0$  becomes a high frequency center input signal  $C_H$ . In a phantom mode, the center input signal  $C$  is applied to the first and the second adder circuits 17 and 18 through the attenuation circuit 24. As a result, the left stereo output signal  $L_0$  becomes  $L + C$ , while the right stereo signal  $R_0$  becomes  $R + C$ . In a wide mode, since the first switch 22 is opened while the second switch 23 selects the center input signal  $C$ ,

the left and the right stereo input signals L and R become the left and right stereo output signals  $L_o$  and  $R_o$ , respectively, and the center output signal  $C_o$  becomes equal to the center input signal C.

Accordingly, the circuit of Fig. 3 enables an accurate center mode controlling.

However, the circuit of Fig. 3 had a problem in that it required the low pass filter 20 and the high pass filter 21, so that adjustment of characteristics of these filters was difficult. In addition, when this circuit is implemented as an integrated circuit, the low pass filter 20, the high pass filter 21 and the first and the second switches 22 and 23 should be externally attached to the IC, thereby increasing the number of pins for external attachment, so that it was not suitable for an integrated circuit.

### SUMMARY OF THE INVENTION

An object of the present invention is to provide a center mode control circuit in which adjustment of filter characteristics is not necessary.

Another object of the present invention is to provide a center mode control circuit having the fewer number of parts to be externally attached, and suitable for an integrated circuit.

A center mode control circuit according to the present invention, in short, comprises an amplifying portion for amplifying a center input signal, a switch connected to an input of the amplifying portion, and capacitors to be selected by the switch and having different characteristics to each other, wherein a normal mode, a phantom mode and a wide mode can be switched by controlling the switch.

According to the present invention, a mode can be selected only by controlling the switch connected to the input end of the amplifying portion. In this case, if the switch is set to the normal position, a capacitor for grounding only high frequency component of the center input signal is connected to the input end of the amplifying portion, so that only low frequency component of the center input signal can be added to the left and the right stereo input signals. If the switch is set to the phantom position, the input end of the amplifying portion becomes open, so that full range of the center input signal can be added to the left and the right stereo input signals. In addition, if the switch is set to the wide position, a capacitor for grounding full range of the center input signal is connected to the input end of the amplifying portion, so that no adding is performed. Therefore, according to the present invention, switching of the switch connected to the input end of the amplifying portion enables selection of three modes.

The foregoing and other objects, features, as-

pects and advantages of the present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings.

### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a circuit diagram showing one embodiment of the present invention;

Fig. 2 is a schematic block diagram showing a surround-decoder system to which a center mode control circuit of the present invention is applied;

Fig. 3 is a circuit diagram showing a conventional center mode control circuit.

### DESCRIPTION OF THE PREFERRED EMBODIMENTS

Fig. 1 is a circuit diagram showing one embodiment of the present invention, wherein 26 denotes a first input terminal to which a left stereo input signal L is applied, 27 denotes a second input terminal to which a center input terminal C is applied, 28 denotes a third input terminal to which a right stereo input signal R is applied, 29 denotes a first adding and amplifying circuit for adding the left stereo input signal L and the center input signal C, 30 denotes a second adding and amplifying circuit for adding the right stereo input signal R and the center input signal C, 31 denotes an inversion amplifier circuit having its positive and negative input terminals connected through a resistor 33 and 32, respectively, to the second input terminal 27, 34 denotes a switch connected to the positive input terminal of the inversion amplifier circuit 31, 35 denotes a first capacitor connected to a second fixed terminal of the switch 34, for grounding high frequency component of the center input signal, 36 denotes a second capacitor connected to a third fixed terminal of the switch 34, for grounding full range of the center input signal.

Now, if a movable contact of the switch 34 is connected to the second fixed contact to select a normal mode, all of the center input signal is applied to the negative input terminal of the inversion amplifier circuit 31 through the resistor 32, while only the low frequency components of the center input signal is applied to the positive input terminal of the inversion amplifier circuit 31. At this time, since the feedback resistor 36 is connected between the negative input terminal and the output terminal of the inversion amplifier circuit 31, the inversion amplifier circuit 31 operates as a negative feedback amplifier circuit, so that a center input signal high frequency component  $C_H$  of the opposite polarity is generated at a second output

terminal 39. The center input signal high frequency component of the opposite polarity is added to the center input signal by means of addition resistors 37 and 38, so that consequently, a center input signal low frequency component  $C_L$  is applied to the first adding and amplifying circuit 29 to be added to the left stereo input signal L. As a result, a sum signal  $L_0$  ( $= L + C_L$ ) of the left stereo input signal L and the center input signal low frequency component  $C_L$  is generated at a first output terminal 40. Similarly, a signal  $R_0$  ( $= R + C_L$ ) obtained by addition of the right stereo input signal R and the center input signal low frequency component  $C_L$  is generated at a third output terminal 41.

Accordingly, in a normal mode, low frequency components of the center input signal is added to the left and the right stereo input signals, so that low frequency compensation becomes possible when as a center speaker a compact one without producing bass is used.

Now, if the movable contact of the switch 34 is connected to a first fixed contact to select a phantom mode, the center input signal is simultaneously applied to the positive and the negative input terminals of the inversion amplifier circuit 31, so that no output signal is generated at the output terminal of the inversion amplifier circuit 31. Therefore, the center input signal C is applied to the first and the second adding and amplifying circuit 29 and 30 through the addition resistances 37 and 38, 42 and 43, respectively, to be added to the left and the right stereo input signals L and R. As a result, a sum signal  $L_0$  ( $= L + C$ ) of the left stereo input signal L and the center input signal C is generated at the first output terminal 40, sum signal  $R_0$  ( $= R + C$ ) of the right stereo input signal R and the center input signal C is generated at the third output terminal 41, and no output signal is generated at the second output terminal 39.

Accordingly, in a phantom mode, the center input signal is added to the left and the right stereo input signals, so that it becomes a suitable mode especially for a system without a center speaker.

In addition, if the movable contact of the switch 34 is connected to a third fixed contact to select a wide mode, the center input signal is applied only to the negative input terminal of the inversion amplifier circuit 31, so that an inverted signal of the center input signal is obtained at the output terminal of the inversion amplifier circuit 31. The inverted signal is added to the center input signal by means of the addition resistors 37, 38, 42 and 43 to be cancelled. Therefore, only left and right stereo input signals L and R are respectively applied to the first and the second adding and amplifying circuits 29 and 30, and the left and the right stereo input signals L and R are generated respectively at the first and the third output terminals 40 and 41 as

the output signals  $L_0$  and  $R_0$ , while the center input signal C is generated as the output signal  $C_0$  at the second output terminal 39.

Accordingly, in a wide mode, the left and the right stereo input signals L and R and the center input signal C are applied respectively to a speaker as the left and the right stereo output signals  $L_0$  and  $R_0$  and the center output signal  $C_0$ , thereby forming a normal surround system.

When adding is performed in the first and the second adding and amplifying circuits 29 and 30, a level of the center input signal C is decreased by 3 dB to be added to the left and the right stereo input signals L and R in order not to increase sound volume in adding. This can be performed by adequately setting ratio of feedback resistors 44 and 45 of the first and the second adding and amplifying circuits 29 and 30, and the addition resistors 37 and 38, 42 and 43.

As described above, according to the embodiments of the present invention, in processing a center input signal to generate a signal to be added to left and right stereo input signals, a center mode control circuit can be comprised of only high pass inversion amplifier circuits, so that setting of characteristics is facilitated, and thereby providing a center mode control circuit with small dispersion. In addition, in integration of circuits, since mode can be switched only by using an input terminal of an amplifying portion as an external pin to be externally connected to a switch, the center mode control circuit according to the present invention is suitable for integration of circuits.

Although the present invention has been described and illustrated in detail, it is clearly understood that the same is by way of illustration and example only and is not to be taken by way of limitation, the spirit and scope of the present invention being limited only by the terms of the appended claims.

## Claims

1. A center mode control circuit for switching of a normal mode for adding low frequency component of a center input signal to left and right stereo input signals, a phantom mode for adding full range of said center input signal to said left and right stereo input signals, and a wide mode for not adding said center input signal to said left and right stereo input signals, comprising:
  - means (31) for amplifying said center input signal,
  - switching means (34) connected to an input terminal of said amplifying means, and
  - capacitor means (35, 36) selected by said switching means and having different characteristics from each other, said mode switching being performed

by controlling of said switching means.

2. A center mode control circuit according to claim 1, wherein

said amplifying means (31) comprising positive and negative input terminals and an output terminal, and

said switching means (34) connected to the positive input terminal of said amplifying means, for switching a state of said positive input terminal to be open, to be connected to the high pass capacitor (35) or connected to the full pass capacitor (36).

3. A center mode control circuit according to claim 1, wherein

said center mode control circuit is formed in a single integrated circuit, and said switching means being connected to an external pin of said integrated circuit.

4. A center mode control circuit comprising:

a first signal path for a left stereo input signal,

a second signal path for a center signal,

a third signal path for a right stereo input signal,

a high pass inversion amplifier means (31) inserted into said second signal path,

a first adder means (29) for adding an input signal and an output signal of said high pass inversion amplifier means to said left stereo input signal applied to said first signal path,

a second adder means (30) for adding the input signal and the output signal of said high pass inversion amplifier means to said right stereo input signal applied to said third signal path, and

switching means (34) for switching so that the output signal of said high pass inversion means becomes any of a high pass signal, a full range signal or a zero signal of said center signal.

5. A center mode control circuit according to claim 4, wherein

said center mode control circuit is formed in a single integrated circuit, and said switching means being connected to an external pin of said integrated circuit.

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FIG. 1

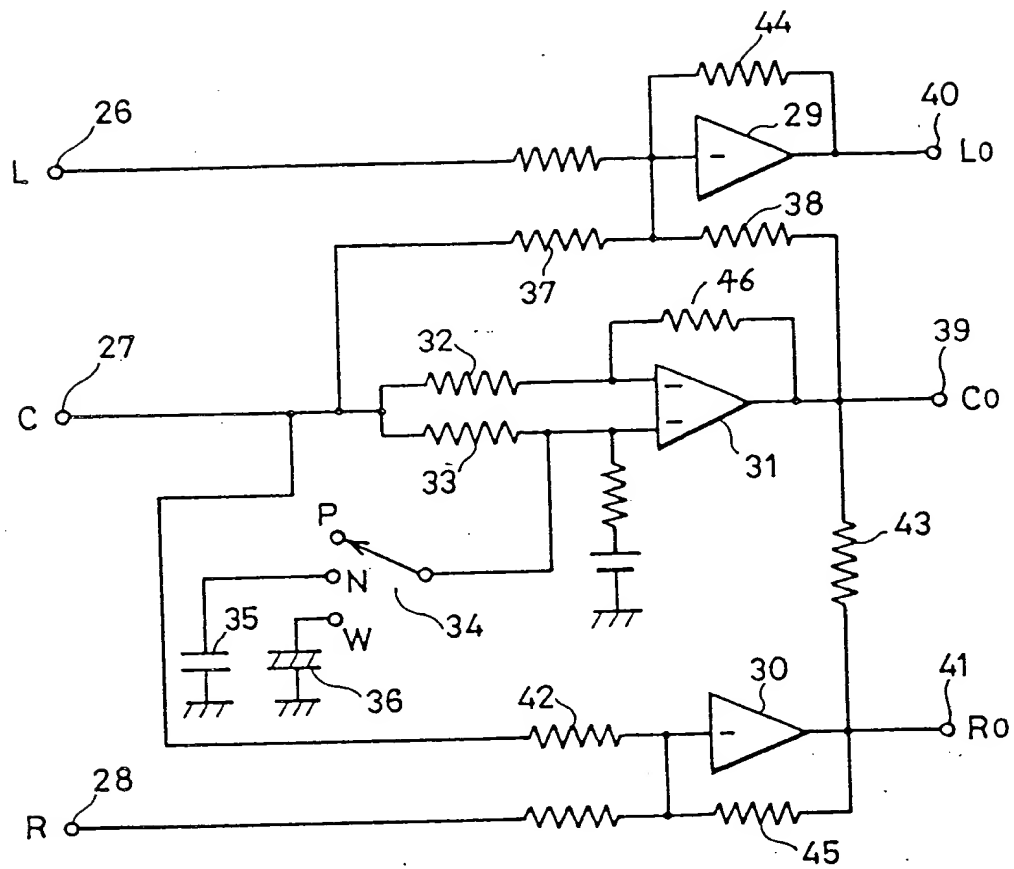


FIG. 3

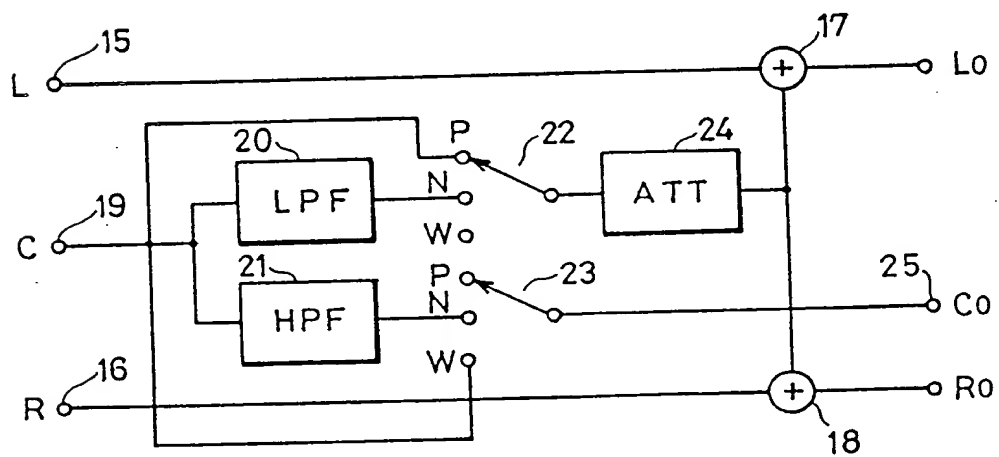


FIG. 2

